

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	981	(trim or trimming) same (clock)	US-PGPUB; USPAT	OR	ON	2005/02/09 10:36
L2	366	(trim or trimming) same (clock) same frequency	US-PGPUB; USPAT	OR	ON	2005/02/09 10:36



US 20040201939A1

(19) **United States**

(12) **Patent Application Publication**

Shipton et al.

(10) Pub. No.: **US 2004/0201939 A1**

(43) Pub. Date: **Oct. 14, 2004**

(54) **CLOCK TRIM MECHANISM FOR
ONBOARD SYSTEM CLOCK**

Publication Classification

(75) Inventors: **Gary Shipton, Balmain (AU); Simon
Robert Walmsley, Balmain (AU)**

(51) Int. Cl.⁷ **H02H 5/04**

(52) U.S. Cl. **361/104**

Correspondence Address:
**SILVERBROOK RESEARCH PTY LTD
393 DARLING STREET
BALMAIN 2041 (AU)**

(57) **ABSTRACT**

(73) Assignee: **SILVERBROOK RESEARCH PTY
LTD**

(21) Appl. No.: **10/727,210**

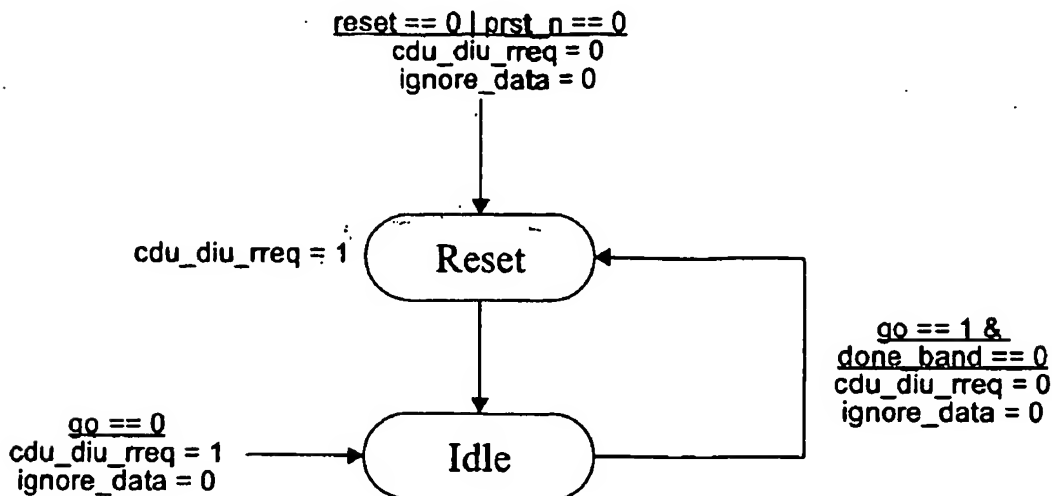
(22) Filed: **Dec. 2, 2003**

(30) **Foreign Application Priority Data**

Dec. 2, 2002 (AU) **2002953134**

Dec. 2, 2002 (AU) **2002953135**

An integrated circuit, comprising a processor, an onboard system clock for generating a clock signal, and clock trim circuitry, the integrated circuit being configured to: (a) receive an external signal; (b) determine either the number of cycles of the clock signal during a predetermined number of cycles of the external signal, or the number of cycles of the external signal during a predetermined number of cycles of the clock signal; (c) store a trim value in the integrated circuit, the trim value having been determined on the basis of the determined number of cycles; and (d) use the trim value to control the internal clock frequency.



Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	50	("5615104" "6260524" "5717886" "4470109" "4628451" "5347309" "5349666" "5539919" "5541667" "5611047" "5634042" "5659719" "5784271" "5798728" "5828361" "5897653" "6195279" "6259629" "6369912" "6404815" "6438036" "4449180" "4259741" "4439769" "4524335" "4910641" "5321734" "5343161" "5349268" "5646492" "5706362" "6009076" "6155062" "4329074" "4398140" "4922527" "5786909" "5535384" "5546382" "4608540" "4775954" "4801899" "4818959" "4819078" "4897839" "5199110" "5255351" "5262971" "5262984" "5329474").pn.	US-PGPUB; USPAT	OR	ON	2005/02/09 14:49

10727210_CLS.txt
Most Frequently Occurring Classifications of Patents Returned
From A Search of 10727210 on February 09, 2005

Original Classifications

2 358/1.9
2 365/185.22

Cross-Reference Classifications

2 365/185.18
2 714/748

Combined Classifications

2 330/10
2 358/1.9
2 365/185.18
2 365/185.22
2 710/36
2 714/748

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10727210 on February 09, 2005

- 2 330/10 (1 OR, 1 XR)
Class 330 : AMPLIFIERS
330/10 MODULATOR-DEMODULATOR-TYPE AMPLIFIER
- 2 358/1.9 (2 OR, 0 XR)
Class 358 : FACSIMILE AND STATIC PRESENTATION PROCESSING
358/1.1 STATIC PRESENTATION PROCESSING (E.G.,
PROCESSING DATA FOR PRINTER, ETC.)
358/1.9 .Attribute control
- 2 365/185.18 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/185.01 FLOATING GATE
365/185.18 .Particular biasing
- 2 365/185.22 (2 OR, 0 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/185.01 FLOATING GATE
365/185.18 .Particular biasing
365/185.2 ..Reference signal (e.g., dummy cell)
365/185.22 ...Verify signal
- 2 710/36 (1 OR, 1 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/36 .Input/output access regulation
- 2 714/748 (0 OR, 2 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/746 .Digital data error correction
714/748 ..Request for retransmission

10727210_QUAL.txt

5615104 90
6260524 87
5717886 86
4470109 86
4628451 86
5347309 86
5349666 86
5539919 86
5541667 86
5611047 86
5634042 86
5659719 86
5784271 86
5798728 86
5828361 86
5897653 86
6195279 86
6259629 86
6369912 86
6404815 86
6438036 86
4449180 86
4259741 84
4439769 84
4524335 84
4910641 84
5321734 84
5343161 84
5349268 84
5646492 84
5706362 84
6009076 84
6155062 84
4329074 83
4398140 83
4922527 83
5786909 83
5535384 82
5546382 82
4608540 82
4775954 82
4801899 82
4818959 82
4819078 82
4897839 82
5199110 82
5255351 82
5262971 82
5262984 82
5329474 82

PLUS Search Results for S/N 10727210, Searched February 09, 2005

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

5615104
6260524
5717886
4470109
4628451
5347309
5349666
5539919
5541667
5611047
5634042
5659719
5784271
5798728
5828361
5897653
6195279
6259629
6369912
6404815
6438036
4449180
4259741
4439769
4524335
4910641
5321734
5343161
5349268
5646492
5706362
6009076
6155062
4329074
4398140
4922527
5786909
5535384
5546382
4608540
4775954
4801899
4818959
4819078
4897839
5199110
5255351
5262971
5262984
5329474